Haja-20-01 Vi Jones

> PATENT P56181

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ju-Heon LEE

Serial No.:

To be assigned

Examiner:

To be assigned

Filed:

11 October 2000

Art Unit:

To be assigned

For:

PORTABLE INTEGRATED CIRCUIT MEMORY DEVICE FOR USE WITH

UNIVERSAL SERIAL BUS

## INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites, provides copies and discusses the following art references:

	Patent No.	<u>Inventor(s)</u>	<b>Issued Date</b>
1.	6,078,978	Suh	6/20/2000
2.	5,887,145	Harari et al.	3/23/1999
3.	6,073,205	Thomson	6/6/2000
4.	6,079,019	Fukuzumi	6/20/2000
5.	6,044,470	Kuriyama	3/28/2000
6.	5,928,347	Jones	7/27/1999
7.	6,044,428	Rayabhari	3/28/2000

Suh '978 discloses a bus interface circuit in a semiconductor memory device.

Harai et al. '145 discloses a removable mother/daughter peripheral card.

Thomson '205 discloses a system and method of write posting in a universal serial bus

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system.

Fukuzumi '019 discloses an IC memory card.

Kuriyama '470 discloses an IC card portable terminal apparatus.

Jones '347 discloses a universal memory card interface apparatus.

Rayabhari '428 discloses a configurable universal serial bus node.

The citation of the foregoing references is not intended to constitute an assertion that other or more relevant art does not exist. Accordingly, the Examiner is requested to make a wideranging and thorough search of the relative arts.

No fee is incurred by this Statement.

Respectfully submitted,

Robert E. Bushnell

Reg. No.: 27,774

1522 "K" Street, N.W., Suite 300 Washington, D.C. 20005 Area Code: 202-408-9040

Folio: P56181

Date: 11 October 2000

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